

COMPUTER DATA LINES

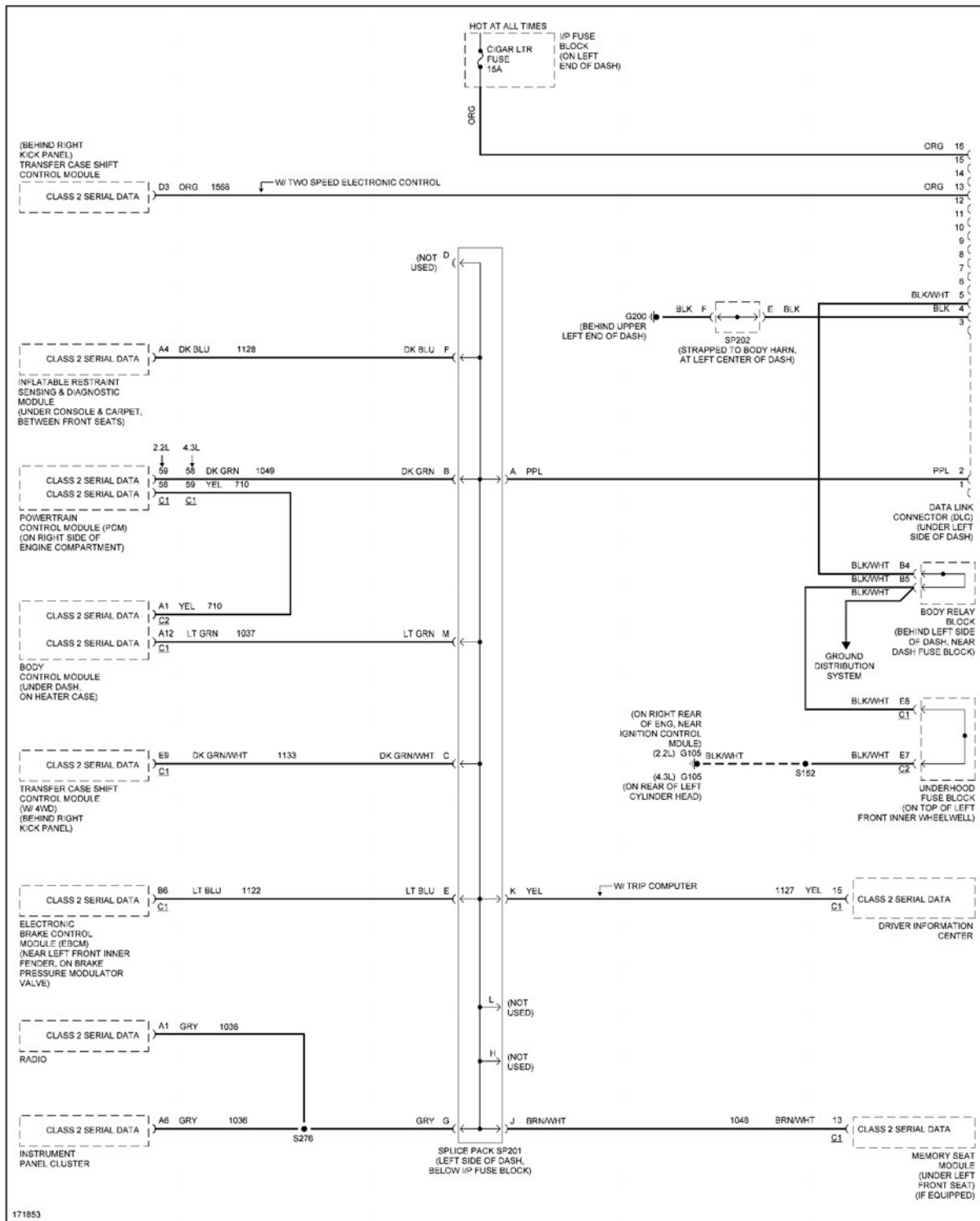


Fig. 7: Computer Data Lines Circuit